

Form PTO-1449 (modified)

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2000.052200/TT4036Serial No.  
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List of Patents and Publications for Applicant's

Applicant  
DALE E. GULICK

## INFORMATION DISCLOSURE STATEMENT

(Use several sheets if necessary)

Filing Date:  
January 11, 2002Group:  
~~unknown~~  
2195U.S. Patent Documents  
See Page 1Foreign Patent Documents  
See Page 1Other Art  
See Page 1

## Other Art (Including Author, Title, Date Pertinent Pages, Etc.)

Exam. Int.	Ref. Des.	Citation
J.T	C22	Intel, "System Management BIOS Reference Specification," Version 2.3.1, March 1999
J.T	C23	Intel, "System Management Bus Specification," Revision 1.0, 02/15/1995
J.T	C24	Intel, "System Management Bus (SMBus) Specification," SBS Implementers Forum, Revision 2.0, 08/03/2000
J.T	C25	J. Postel, "User Datagram Protocol," ISI 08/28/1980
J.T	C26	"About the 8051 Microcontroller," pp. 1-16 <i>no date</i>
J.T	C27	Intel, "8259A Programmable Interrupt Controller (8259A/8259A-2)," December, 1988
J.T	C28	Intel, "Intel® 840 Chipset Platform Design Guide," October, 1999
J.T	C29	AMD, "AMD's Altera™ Technology for Advanced Systems Management," Publication No. 22297 Rev. A, December, 1998
J.T	C30	AMD, "AMD-766™ Peripheral Bus Controller Data Sheet," 23167B March, 2001

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